

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				ATTY. DOCKET NO. TI-30961	SERIAL NO. 09/703,430		
LIST OF DOCUMENTS CITED BY APPLICANT <i>(Use several sheets if necessary)</i>				APPLICANT James B. Cho, et al.			
				FILING DATE 10/31/00	GROUP 7666 2683		
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
	AI						
	AJ						
	AK						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
<i>FD</i>	AL	J. Christoph Scheytt, "A 0.155-, 0.622-, and 2.488-Gb/s Automatic Bit-Rate Selecting Clock and Data Recovery IC for Bit-Rate Transparent SDH Systems" IEEE Journal of Solid-State Circuits, Vol. 34, No. 12, pp. 1935-1943, December 1999					
<i>FD</i>	AM	OC-48/24/12/3 Sonet/SDH Multi-Rate Transceiver, Texas Instruments Product Review Revision 3.1					
	AN						
	AO						
	AP						
EXAMINER	<i>MC One</i>				DATE CONSIDERED <i>9/23/04</i>		
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

